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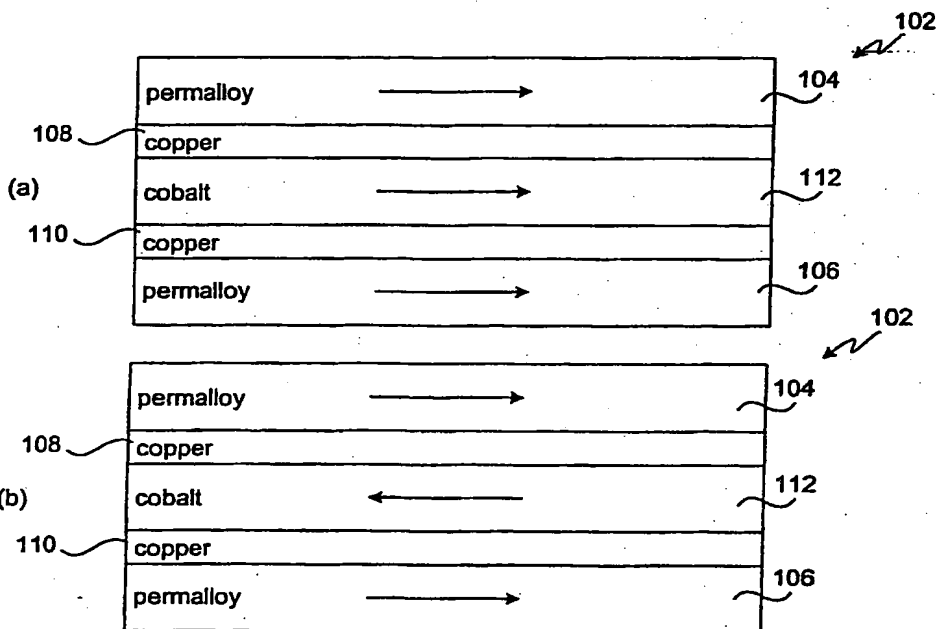
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(54) Title: MAGNETORESISTIVE TRIMMING OF GMR CIRCUITS



(57) Abstract: Methods and apparatus are described relating to an electronic device which includes at least one configurable resistive element (104, 106, 112). Each such configurable resistive element includes at least one multi-layer thin film (104, 106) element exhibiting giant magnetoresistance. The resistance value of each of configurable resistive element (104, 106) is configurable over a resistance value range by application of at least one magnetic field which manipulates at least one magnetization vector associated with the thin film element (104, 106).

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— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

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PCT/US01/20665**A. CLASSIFICATION OF SUBJECT MATTER**

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US CL :365/158, 173; 360/324.12, 325

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 365/158, 173; 360/324.12, 325

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
NONEElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
USPTO APS EAST**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,587,943 A (TOROK et al) 24 December 1996 (24.12.1996), see entire document.	1-19
X,E	US 6,278,594 B1 (ENGEL et al) 21 August 2001 (21.08.2001), see entire document.	1-19
X,E	US 6,292,336 B1 (HORN et al) 18 September 2001 (18.09.2001), see entire document.	1-19

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